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IDENTIFICATION

PRODUCT CODE: AC-R839E-MC
PRODUCT NAME: CXCRAF0 CR11 SCAN MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

CRA IS A BKMOD THAT WILL EXERCISE UP TO "N" CR11 SCAN MODULES HAVING CONTIGUOUS UNIVRS ADDRESSES. NON-CONTIGUOUS GROUPS OF SCAN MODULES MAY BE EXERCISED BY CONFIGURING THE CRA MODULE FOR EACH GROUP. THE MODULE SIMPLY TESTS THE AVAILITY OF THE MAINTENANCE FLOPS TO ACTIVATE AND DEACTIVATE ALL SCN LINES IN ALL SCAN REGISTERS SELECTED FOR TEST. IF ANY LINE FAILS TO SET OR CLEAR PROPERLY THE ERROR IS REPORTED VIA THE CONSOLE TTY.

2. REQUIREMENTS

HARDWARE: A CR11 INTERFACE WITH AT LEAST ONE SCAN MODULE

STORAGE: CRA REQUIRES:

- 1. DECIMAL WORDS: 202
- 2. OCTAL WORDS: 0312
- 3. OCTAL BYTES: 624

3. PASS DEFINITION:

ONE PASS OF THE CRA MODULE RESULTS IN 100. ITERATIONS OF THE BASIC TEST SEQUENCE WHICH CLEARS AND SETS ALL SCAN REGISTERS SELECTED FOR TEST.

4. EXECUTION TIME

CRA RUNNING ALONE ON A PDP11/05 SYSTEM WITH ONE SCAN MODULE TAKES LESS THAN 10 SECONDS TO COMPLETE ONE PASS.

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DATA:0,VECTOR:0,BR1:0,BR2:0,DEVcnt:0,SRI:0

REQUIRED PARAMETERS:

FOR EACH COPY OF CRA CONFIGURED THE USER MUST SPECIFY:

DVA= THE FIRST ADDRESS OF THE FIRST SCAN REGISTER IN A CONTIGUOUS GROUP

SRI= THE NO. (OCTAL) OF SCAN MODULES IN THE GROUP.

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SEQ 0003

THIS SHOULD BE A STRAIGHT OCTAL NUMMR, NOT A
BIT MAP AS IS USUALLY DONE IN DEVCNT. DEVCNT
WAS NOT USED FOR THE DEVICE COUNT BECAUSE IT
CAN ONLY HOLD 16 AS A MAXIMUM NUMBER OF DEVICES
AND CBA CAN RUN UP TO 256 DEVICES

6. DEVICE OPTION SETUP

ALL EXTERNAL LINES ENTERING THE SCAN INPUTS FROM THE PLANT MUST BE DISCONNECTED.

7. MODULE OPERATION

TEST SEQUENCE:

- A. SET UP THE PASS COUNTER FOR 100. ITERATIONS
- B. GET SRI TO FIND OUT HOW MANY SCAN MODULES TO TEST.
- C. CLEAR A SCAN REGISTER
- D. COUNT IT
- E. TEST FOR ALL BITS CLEARED - REPORT ANY ERROR
- F. GENERATE NEXT REGISTER ADDRESS
- G. REPEAT C-F UNTIL ALL LINES TESTED
- H. SET A SCAN REGISTER
- I. TEST FOR ALL BITS SET - REPORT ANY ERROR
- J. REPEAT I-J UNTIL ALL LINES TESTED
- K. COUNT ONE ITERATION
- L. IF NOT 100. REPEAT B-K
- M. REPORT END OF PASS RESTART AT A

SUBROUTINES:

STAL: TIMER TO ALLOW SCAN LINES TO SETTLE AFTER SETTING THE MAINTENANCE FLOPS

BPK: TIMER TO PREVENT RESCANNING MORE OFTEN THAN ONCE EVERY 50 MSEC

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8. OPERATION OPTIONS

A. USER CAN MODIFY LOCATIONS "ADDR" AND "SRI" TO SELECT
ANY GROUP OF SCAN MODULES

9. NON-STANDARD PRINTOUTS

NONE: ALL PRINTOUTS HAVE THE STANDARD DEC/X11 FORMATS.

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FDEC/X11 CB11 SCAN MODULE EXERCISER
000000- BKMOD <CBAE > 100.33
000000- MODULE 40020,CBAE 100.33
          .TITLE CBAE DEC/X11 SYSTEM EXERCISER MODULE
          .DDACOM VERSION 6 23-NOV-78
          .LIST BIN
*****
000000- REGIN:
000000- 041103 042501 040 MODNAM: -ASCII /CBAE /MODULE NAME
000005- 000         XFLAG: -BYTE OPEN ;USED TO KEEP TRACK OF WBOFF USAGE
000006- 000000         ADDR: +0 ;1ST DEVICE ADDR.
000010- 000000         VECTOR: +0 ;1ST DEVICE VECTOR.
000012- 000         BR1: -BYTE PRVY+0 ;1ST BR LEVEL.
000013- 000         BR2: -BYTE PRVY+0 ;2ND BR LEVEL.
000014- 000001         DVID1: +1 ;DEVICE INDICATOR 1.
000016- 000000         SR1: OPEN ;SWITCH REGISTER 1.
000020- 000000         SR2: OPEN ;SWITCH REGISTER 2.
000022- 000000         SR3: OPEN ;SWITCH REGISTER 3.
000024- 000000         SR4: OPEN ;SWITCH REGISTER 4.
*****
000026- 040020 STAT: 40020 ;STATUS WORD.
000030- 000224- LMT: START ;MODULE START ADDR.
000032- 000224- SPOINT: MODSP ;MODULE STACK POINTER.
000034- 000000 PASCNT: 0 ;PASS COUNTER.
000036- 000144 ICNT: 100. ;# OF ITERATIONS PER PASS=100.
000040- 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS.
000042- 000000 HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS.
000044- 000000 SOFPAS: 0 ;LOC TO SAVE TOTAL HARD ERRORS.
000046- 000000 SWPASP: 0 ;LOC TO SAVE SOFT ERRORS PER PASS.
000050- 000000 SYSCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS.
000052- 000000 RANUM: 0 ;# OF SVS ERRORS ACCUMULATED.
000054- 000000 CONFIG: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED.
000056- 000000 RES1: 0 ;RESERVED FOR MONITOR USE.
000060- 000000 RES2: 0 ;RESERVED FOR MONITOR USE.
000062- 000000 SVR0: OPEN ;LOC TO SAVE R0.
000064- 000000 SVR1: OPEN ;LOC TO SAVE R1.
000066- 000000 SVR2: OPEN ;LOC TO SAVE R2.
000070- 000000 SVR3: OPEN ;LOC TO SAVE R3.
000072- 000000 SVR4: OPEN ;LOC TO SAVE R4.
000074- 000000 SVR5: OPEN ;LOC TO SAVE R5.
000076- 000000 SVR6: OPEN ;LOC TO SAVE R6.
00100- 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
00102- 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR
00104- 000000 ACSR: OPEN ;CONTENTS OF CSR.
00106- 000000 WSBADR: OPEN ;ADDR OF BAD DATA, OR
00108- 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
00110- 000000 ERRTP: OPEN ;TYPE OF ERROR.
00112- 000266- ASB: OPEN ;EXPECTED DATA.
00114- 000000 ANAS: OPEN ;ACTUAL DATA.
00116- 000000 RSTRT: RSTRT ;RSTART ADDRESS AFTER END OF PASS.
00118- 000000 WDTO: OPEN ;WORDS TO MEMORY PER ITERATION.
00120- 000000 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION.
00122- 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION.

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000122- 000033 IDNUM: 33 ;MODULE IDENTIFICATION NUMBER=33
000040 000040 .REPT SPSIZ ;MODULE STACK STARTS HERE.
          .NLIST
          .WORD 0
          .LIST
          .ENDR
000224- MODSP:
*****
234 000224- 012767 000002 177664 START: MOV #2,WDFR ;2 WORDS FR MEM/ITERATION
235 000232- 012767 000025 177646 MOV #25,ERRTP ;BIT STUCK IN REG ONLY ERROR
236 000240- 014100 000000 BRQ SR1,R2 ;SAVE SR1
238 000244- 001410 000000 BRQ SR1,R2 ;SAVE SR2
239 000246- 062767 000002 177640 1S: ADD #2,WDTO ;INCREASE WDTO COUNT
240 000254- 062767 000002 177634 ADD #2,WDFR ;INCREASE WDFR COUNT
241 000262- 05302 000000 DEC R0 ;
242 000264- 001370 000000 BNE 1S ;NOT DONE BRANCH
243 000266- 016700 177514 RESTR: MOV ADDR,R0 ;R0 POINTS TO 1ST SCAN REGISTER
244 000272- 016702 177520 MOV SR1,R2 ;GET THE SCAN MODULE COUNT
245 000276- 01701 177574 1S: MOV #4,R1 ;COUNT 4 REGISTERS PER MODULE
246 000302- 005200 000017 (R0) ;POINT RO TO MAINT. REGISTER
247 000304- 152710 000017 CMPB #17,(R0) ;GET BITS <11:08> IN THE MAINT REG.
248 000310- 122710 000017 (R1),(R0) ;DID ALL FOUR BITS SET ??
249 000314- 001402 000130 BR IF YES ;BR IF YES
250 000316- 004767 000252 JSR PC,ERR1 ;GO REPORT IT
251 000322- 004767 000252 JSR PC,STAL ;GO REPORT FOR SCAN LINES TO SETTLE
252 000326- 005300 000132 4S: DEC RO ;POINT RO BACK AT 1ST REG.
253 000330- 005710 000132 TST (R0) ;ALL BITS CLEARED ?
254 000332- 001402 000132 BR EQ 3S ;BR IF YES
255 000334- 004767 000132 JSR PC,ERR2 ;GO REPORT IT
256 000340- 005720 000132 3S: TST (R0)+ ;GENERATE THE NXT ADDRESS
257 000342- 005201 000174 INC R1 ;COUNT ONE OF 4
258 000344- 001371 000174 BNE 4S ;BR IF NOT 4
259 000346- 005302 000174 JSR PC,BRK ;GO TAKE A BREAK
260 000350- 001372 000174 INC R2 ;COUNT ONE SCAN MODULE LEFT
261 000352- 004767 000174 BNE 1S ;BR IF ANY MODULES LEFT
262 000356- 016702 177434 T2: JSR PC,BRK ;GO TAKE A BREAK
263 000362- 016700 177420 MOV ADDR,R0 ;GET THE SCAN MODULE COUNT
264 000366- 012701 177774 1S: MOV #4,R1 ;COUNT 4 REGS PER MODULE
265 000372- 005200 000174 INC (R0) ;POINT RO TO MAINT. REG.
266 000374- 105010 000104 CLR R0 ;CLEAR BITS <11:08> IN THE MAINT. REG.
267 000376- 105710 000104 TSTR (R0) ;DID ALL FOUR CLEAR ??
268 000400- 001402 000104 BEQ 2S ;BR IF YES
269 000402- 004767 000166 JSR PC,ERR3 ;GO REPORT IT
270 000406- 004767 000166 JSR PC,STAL ;GO WAIT FOR SCAN LINES TO SETTLE
271 000412- 005300 177777 4S: DEC RO ;POINT RO BACK TO 1ST REG.
272 000414- 022710 177777 CMP #17777,(R0) ;ALL BITS SET?
273 000418- 001402 000104 BEQ 3S ;BR IF YES
274 000422- 004767 000104 JSR PC,ERR4 ;GO REPORT IT
275 000426- 005720 000104 TST (R0)+ ;GENERATE THE NXT ADDRESS
276 000430- 005201 000104 INC R1 ;COUNT OF 4
277 000432- 001370 000104 BNE 4S ;BR IN NOT 4
278 000434- 005302 000104 JSR PC,BRK ;COUNT ONE SCAN MODULE LEFT
279 000436- 001353 000106 BNE 1S ;BR IF ANY MODULES LEFT
280 000440- 004767 000106 JSR PC,BRK ;GO TAKE A BREAK
281 000444- 104413 000000- ENDITS,BEGIN ;SIGNAL END OF ITERATION.

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282  
283 000450 000706 BR RESTRY ;MONITOR SHALL TEST END OF PASS  
284 ;BR IF NOT 100. TIMES  
285  
286  
287 ;ROUTINE TO REPORT FAILURE TO CLEAR BITS <11:8> IN MAINT. REGISTER  
288  
289 000452 010067 177422 ERR1: MOV R0,CSRA ;CSRA = MAINT. REG ADDRESS  
290 000456 011067 177426 MOV (R0),ACSR ;ACSR=BITS <11:8> OF MAINT. REG.  
291 ;*****  
292 000462 104405 000000 000000 HDRS,BEGIN,NULL ;CAN'T CLEAR (11:8) IN MAINT REG  
293 ;*****  
294 000470 000207 RTS PC ;CONTINUE WITH EXERCISE  
295  
296 ;ROUTINE TO REPORT FAILURE TO CLEAR ALL BITS IN SCAN REG.  
297  
298 000472 010067 177402 ERR2: MOV R0,CSRA ;CSRA=ADDRESS OF SCAN REG.  
299 000476 011067 177406 MOV (R0),ACSR ;ACSR=CONTENTS OF SCAN REG.  
300 ;*****  
301 000502 104405 000000 000000 HDRS,BEGIN,NULL ;REG SHOULD BE ALL ZEROS  
302 ;*****  
303 000510 000207 RTS PC ;CONTINUE WITH EXERCISE  
304  
305 ;ROUTINE TO REPORT FAILURE TO SET ALL BITS <11:8> IN MAINT REG  
306  
307 000512 010067 177362 ERR3: MOV R0,CSRA ;CSRA=ADDRESS OF MAINT. REG.  
308 000516 011067 177366 MOV (R0),ACSR ;ACSR=CONTENTS OF MAINT. REG.  
309 ;*****  
310 000522 104405 000000 000000 HDRS,BEGIN,NULL ;BITS (11:8) SHOULD BE ALL ONES  
311 ;*****  
312 000530 000207 RTS PC ;CONTINUE WITH EXERCISE  
313  
314 ;ROUTINE TO REPORT FAILURE TO CLEAR ALL BITS IN SCAN REG.  
315  
316 000532 010067 177342 ERR4: MOV R0,CSRA ;CSRA=ADDRESS OF SCAN REG.  
317 000536 011067 177346 MOV (R0),ACSR ;ACSR=CONTENTS OF SCAN REG.  
318 ;*****  
319 000542 104405 000000 000000 HDRS,BEGIN,NULL ;REG SHOULD BE ALL ONES  
320 ;*****  
321 000550 000207 RTS PC ;CONTINUE WITH EXERCISE  
322  
323 ;ROUTINE TO INSURE SCAN LINES ARE NOT SAMPLED MORE OFTEN  
324 ;THAN ONCE EVERY 50 USEC.  
325  
326 000552 012767 000031 000042 BRK: MOV #25.,STALL ;SET STALL COUNT TO 25.  
327 IS: ;  
328 000560 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...  
329 000564 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.  
330 000570 005367 000026 DEC STALL ;TICK-TOCK  
331 BNE IS ;BR IF STALL NOT 0  
332 RTS PC ;RETURN TO CALLER  
333  
334 ;SUBROUTINE TO INTRODUCE ADEQUATE DELAY TO ALLOW SCAN LINES TO  
335 ;SETTLE DOWN AFTER SETTING THE MAINTENANCE FLOPS  
336  
337
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338  
339 000600 012703 000010 STAL: MOV #10,R3 ;SET UP A COUNT OF 10  
340 000604 IS: ;  
341 000604 104407 000000 BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...  
342 000610 104407 000000 BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.  
343 000614 005303 DEC R3 ;TICK-TOCK GOES THE CLOCK  
344 000616 001372 BNE IS ;BR IF R3 NOT ZERO  
345 000620 000207 RTS PC ;NOW GO BACK AND START TESTING  
346  
347 000622 000000 STALL: OPEN ;STALL COUNTER  
348 000001 .END  
349
```


WDTO 000114R 223# 239*
XFLAG 000005R 181#

. ABS. 000000 000
000624 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XCBAEO, XCBAEO/SOL/CRF:SYM=DDXCON, XCBAEO
RUN-TIME: 11.2 SECONDS
RUN-TIME RATIO: 9/2=3.3
CORE USED: 7K (13 PAGES)